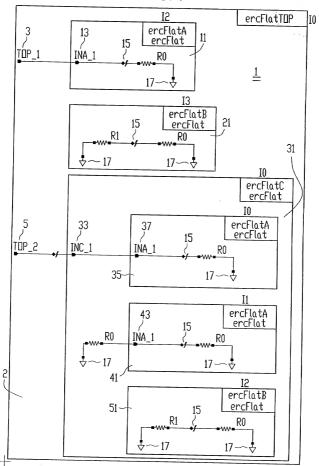
FIG. 1



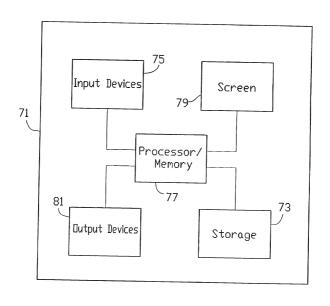
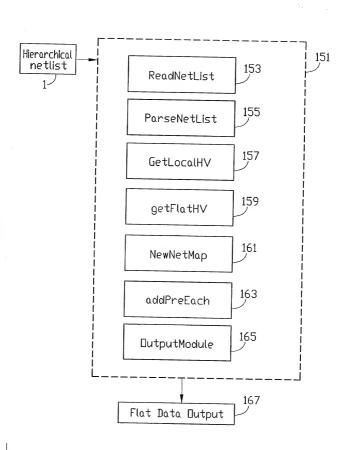


FIG. 3

	TheNetList]1
Parsed Representation of Netlist File		89
	%CELLS	91
	%CURRCELL	99
	%INST	101
	Flat Path Data	102
	%HV	103
	%FlatHV	107
	Flat Data Cache	108 109
	%HVCache	J
	%SEEN	111



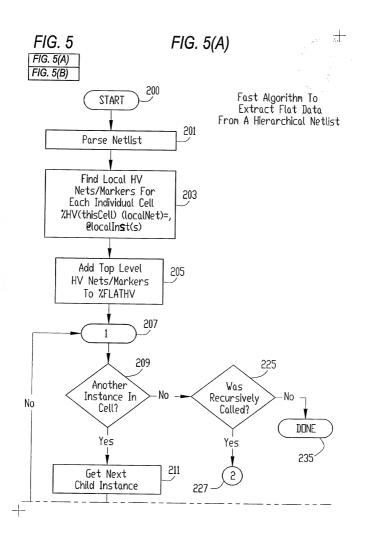
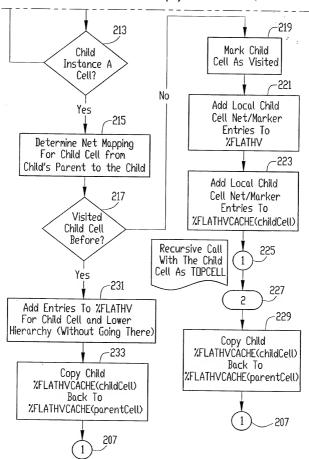


FIG. 5(B)



```
Flat
    INA_1---->(X0)
  Check
  Deep
Cell=<ercFlatB>
  Flat
  Check
  Deep
    net2---->( X0 )
Cell=<ercFlatTOP>
  Flat
  Check
    TOP 1---->( I2/X0 )
    TOP_2---> ( IO/IO/XO )
                                           305
  Deep
    IO/net2---->( IO/I1/X0 )
    I0/I2/net2---->( I0/I2/X0 )
    I3/net2----> (I3/X0)
Cell=<ercFlatC>
  Flat
  Check
    net2---->( I1/X0 )
    INC_1---->( I0/X0 )
  Deep
    I2/net2----> (I2/X0)
```

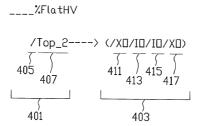
FIG. 6(A) FIG. 6(B) FIG. 6(A)

PROGRAM DUTPUT

```
-----%HV...
        inst=<ercFlatA>
          \langle INA_1 \rangle ---->\langle \langle X0 \rangle \rangle
        inst=<ercFlatB>
                                                        301
          \langle net2 \rangle ----- \rangle (\langle X0 \rangle)
        inst=<ercFlatTOP>
          (TDP 2> ---->( <X0> )
                 -----*FLATHV...
        /I0/net2---->( /I0/I1/X0 )
top
        /TOP 1---->( /I2/X0 )
path
        /I0/I2/net2---->( /I0/I2/X0 )
keys
        I3/net2---->(I3/X0)
                                                            303
        /TOP_2---->( /X0 /I0/I0/X0 )
                         ----%FLATHVCACHF...
        Cell=<ercFlatA>
```

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FIG. 7



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